

16 BIT Programmable Gated-Oscillator

SERIES: PDO-1600

**data
delay
devices, inc.**



FEATURES:

- Completely TTL interfaced and buffered.
- Enable pin provided.
- Microprocessor or computer controlled.
- VCO replacement.
- Reliable and repeatable frequency generation.
- Very stable frequency.
- Jitter-free



Specifications:

- Output fan-out: TTL Schottky loads.
- Period variation: monotonic in one direction.
- Duty cycle: 50%.
- Period tolerance: $\pm 5\%$.
- Inherent period = period increment.
- Power dissipation: 1.1 W max.
- Supply voltage: 4.75 to 5.25 Vdc.
- Operating temperature: 0-70° C.
- Temperature coefficient: 100 PPM/° C.
- Inherent delay (T_{DO}) = 12 ns.

Part Number	Incremental Period (ns)	Upper Frequency (Mhz)
PDO-1600-5	100	5.0
PDO-1600-10	50	10.0

